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			ZUBAJLO, JENNIFER L		
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The time period for reply, if any, is set in the attached communication.

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## DETAILED ACTION

## Response to Arguments

 Applicant's arguments filed 2/26/2008 have been fully considered but they are not persuasive.

2. In response to Applicant's argument that "FIGs. 3 and 4 show the first and second intervals in a sequential order and not in an order that reduces dead times between the time intervals", it should be noted that as broadly interpreted Figures 3 and 4 do teach first and second time intervals in an order that reduces dead times between the time intervals. Note that figures 3 and 4 show an order of time intervals in an MLA scheme and paragraph [0005] states that in an MLA scheme, dead times between the subfields are minimized by proper algorithms.

Also, in response to Applicant's argument that "paragraph [0005] does state that 
"in an MLA scheme, dead times between the subfields are minimized by proper 
algorithms" but does not disclose or suggest an ordering that reduces dead times 
between the time intervals", note that paragraph [0005] alone does not disclose this, 
however when combined with figures 3 and 4 as described above, first and second time 
intervals in an order that reduces dead times between the time intervals is taught.

The same response can be made in regard to Applicant's arguments with respect to paragraphs (0037) and (0039).

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Therefore, with respect to Applicant's argument that "the reliance on AAPA is misplaced", Examiner disagrees. As broadly interpreted, it is clearly explained above how AAPA reads on the limitation of the claims "the generating means/controller generates the first and second time intervals in an order that reduces dead times between the time intervals" as previously filed.

- 3. In response to applicant's argument that the references fail to show certain features of applicant's invention, it is noted that the features upon which applicant relies (i.e., "a multilevel power addressing scheme" and "the individual time intervals SF are in fact used "n" times") are not recited in the rejected claim(s). Although the claims are interpreted in light of the specification, limitations from the specification are not read into the claims. See *In re Van Geuns*, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993).
- 4. In response to Applicant's argument that "FIGs 3 and 4 do not show a multilevel power addressing scheme wherein during time intervals of a frame period, at least a first non-zero emission level of a light emitting element during a first one of the time intervals and a second non-zero emission level during a second one of the time intervals", Examiner refers Applicant back to previous rejection, where during time intervals of a frame period, at least a first non-zero emission level of a light emitting element during a first one of the time intervals and a second non-zero emission level during a second one of the time intervals is taught by figures 10-13 & column 8 lines 1-8 and 38-55 of Khormaei not figures 3 and 4 of AAPA.

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5. In response to Applicant's argument that "the device of claim 1 is not anticipated or made obvious by the teaches of Khormaei in view of AAPA", Examiner disagrees. As broadly interpreted, the combination of these reference does disclose the inventions as claimed in claims 1, 11, and 12 for the reasons as described above.

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to JENNIFER ZUBAJLO whose telephone number is (571)270-1551. The examiner can normally be reached on Monday-Friday, 8 am - 5 pm, EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amare Mengistu can be reached on (571) 272-7674. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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JZ 3/4/08

> /Amare Mengistu/ Supervisory Patent Examiner, Art Unit 2629